

ISSUE CLASSIFICATION	
Class	Subclass

**PATENT NUMBER**

## U.S. UTILITY Patent Application

**O.L.P.E.**

**PATENT DATE**

SCANNED

**Q.A.**

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/84 1536		712	34	2183	Edwards

## APPLICANTS

**DATE OF INVENTION:**  
Sanjay Agarwal  
Sapna Agrawal

in a processor architecture for control processors using synchronous

PTO-2040  
12/89

## ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 14	Figs. Drwg. 3	Print Fig. 3	Total Claims 3
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
	_____ (Date)			

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